

The example below is showing you how to read data from a text file. (The example is from textbook page 300. we corrected some mistakes.)

The file name of the source code is “testfill.vhd”. In this file, we will read data from a text file(“mem1.txt”).

File “mem1.txt”

```
01AC 7
AE 03 B6 91 C7 00 0C
005B 2
01 FC
```

File “testfill.vhd”

```
library ieee, std;
use ieee.std_logic_1164.all;
use ieee.std_logic_arith.all;
use std.textio.all;

entity testfill is
end testfill;

architecture fillmem of testfill is

type RAMtype is array(0 to 2047) of std_logic_vector(7 downto 0);

signal mem: RAMtype :=(others=>(others=>'0'));
--signal testsignal,test1: integer;

procedure fill_memory(signal mem: inout RAMtype) is
type HexTable is array(character range<>) of integer;

constant lookup: HexTable('0' to 'F'):= (0,1,2,3,4,5,6,7,8,9,-1,-1,-1,-1,-1,-1,-1,10,11,12,13,14,15);

file infile: text open read_mode is "mem1.txt";

--reading

variable buff: line;
variable addr_s: string(5 downto 1);
variable data_s: string(3 downto 1);
variable addr1, byte_cnt: integer;
variable data: integer range 255 downto 0;

begin

    while( not endfile(infile)) loop
        readline(infile, buff);
        read(buff, addr_s);
```

```

        read(buff, byte_cnt);
        addr1 := lookup(addr_s(5))*4096 +
lookup(addr_s(4))*256
            + lookup(addr_s(3))*16 + lookup(addr_s(2));

        readline(infile, buff);
        for i in 1 to byte_cnt loop
            read(buff, data_s);

            data:=lookup(data_s(3))*16 +
lookup(data_s(2));
            mem(addr1)<=CONV_STD_LOGIC_VECTOR(data,
8);
            addr1:=addr1+1;
        end loop;
    end loop;
    --testsignal <= byte_cnt;
    end fill_memory;

begin
testbench: process
begin
wait for 10 ns;
fill_memory(mem);
end process;
end fillmem;

```

This is the simulation results:

